## BASE de DATOS: Para redacción de SOCs

[**https://addi.ehu.es/handle/10810/29427**](https://addi.ehu.es/handle/10810/29427) **https://www.researchgate.net/publication/346452253\_New\_applications\_and\_architectures\_based\_on\_FPGASoC**

**-Embedded processors on FPGA: Hard-core vs Soft-core.**Vazhoth Kanhiroth, Vivek J., "Embedded processors on FPGA: Hard-core vs Soft-core" (2017). Masters Theses. 845. <http://scholarworks.gvsu.edu/theses/845>  
  
-A Study of FPGA-based System-on-Chip Designs for Real-Time Industrial Application. DOI: [10.5120/ijca2017913544](http://dx.doi.org/10.5120/ijca2017913544)  
  
file:///C:/Users/luisw/Downloads/New\_applications\_and\_architectures\_based\_on\_FPGASo.pdf

# **-Soft-core embedded FPGA based system on chip**

<https://doi.org/10.1007/s10470-021-01872-5>

Hardcore Vs Softcores  
**Embedded Processors on FPGA: Soft vs Hard**  
<http://people.se.cmich.edu/yelam1k/asee/proceedings/2019/1/6.pdf>

En este aquí dentro fotos para escribir diferencias sobre softcores y hardcores.  
**-Soft-core embedded FPGA based system on chip**